

present invention. Attached hereto is a marked-up version of the changes made to the claims by the current Amendment. The attached page is captioned "VERSION WITH MARKINGS TO SHOW CHANGES MADE."

In the above Office Action, the Examiner has rejected claims 1, 5, 7, 8, 13, 15, 16 and 19 under 35 U.S.C. §102(e) as being anticipated by Kannen (U.S. Patent No. 5,511,205). A portion of the Examiner's characterization of Kannen made in support of the outstanding rejection of claims 1, 8 and 16 is set forth below:

Kannen discloses one or more switching mechanisms to selectively couple one or more of the plurality of input/output devices to one or more of the first plurality of electronic components and enable the apparatus to start up in the instant on mode of operation to the exclusion of the second plurality of electronic components, or to selectively couple the one or more input/output devices to one or more of the second plurality of electronic components and enable the apparatus to start up in the non-instant on mode of operation to the exclusion of the first plurality of components . . . .

Applicant observes that the Examiner has not provided any specific reference to the Kannen specification in support of the position that Kannen describes "one or more switching mechanisms". In this regard the Examiner's reference to "column 4, lines 22-30" at the end of the paragraph in the above Office Action containing the above excerpt refers only to the "Brief Description of Drawings" section in Kannen, which appears to lack any description of switching mechanisms. Applicant respectfully submits that Kannen does not describe or suggest the use of switching mechanisms configured to effect the purpose identified by claims 1, 8 and 16; namely, to selectively couple input and output devices to one or more of a first plurality of components/blocks used in an instant on mode of operation (to the exclusion of a second set of components/blocks) or to selectively couple the input and output devices to one or more of the second plurality of components/blocks used in a non-instant on mode of operation (to the exclusion of the first plurality of components/blocks). For example, Kannen does not describe or suggest structure corresponding to Applicant's keyboard/mouse switch 308, display switch 310, or other mechanical or electrical switching arrangement capable of effecting similar functionality (see, e.g., the present specification at pages 16-18).

Notwithstanding the existence of this clear distinction between the invention as previously claimed and the cited prior art, claims 1 and 8 have been amended so as to clarify a particular operative characteristic of the invention. Namely, amended claims 1 and 8 now recite, as is already recited by pending claim 16, that the claimed input/output or external devices are capable of being *used* both with a first plurality of electronic components during an instant on mode of operation and with a second plurality of electronic components during a non-instant on

mode operation. Although this amendment should not be construed as acquiescence in the outstanding rejection and merely clarifies the nature of a particular operative characteristic of the present invention, it is noted that the cited references do not describe or suggest a similar operative characteristic. For example, in the allegedly "instant on" mode of Kannen identified by the Examiner (col. 12, lines 19-28), the I/O devices attached to the applicable power planes (e.g., the 12 volt plane 204 and the keyboard plane 220) are not powered up (and thus incapable of being used) and only the battery microprocessor enters a "normal" mode of operation (col. 12, lines 27-28). That is, it is not until entry into the allegedly "non-instant on" mode of operation identified by the Examiner (col. 12, lines 29-44) that the I/O devices are capable of actually being used:

During this initial entry into normal state 508, all power planes that have I/O devices attached to them-specifically, 12 volt plane 204 and keyboard plane 220 also receive power. Other peripherals such as the SCSI plane 218 are powered up if they are required by the application.

[col. 12, lines 39-44]

Applicant observes that all of the other pending claims either directly or indirectly include recitations similar to the recitation in amended claims 1 and 8 discussed above. Accordingly, it is respectfully submitted that all of the pending claims define patentable subject matter in view of the cited prior art.

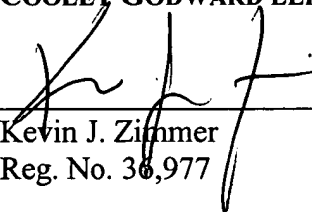
Applicant respectfully requests entry of the amendments described herein prior to further examination of the above-identified application. The undersigned would of course be available to discuss the present application with the Examiner if, in the opinion of the Examiner, such a discussion could lead to resolution of any outstanding issues.

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Respectfully submitted,  
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## VERSION WITH MARKINGS TO SHOW CHANGES MADE

**In the Claims**

1. (Twice Amended) An apparatus comprising:
  - a first plurality of electronic components defining an instant on mode of operation;
  - a second plurality of electronic components defining a non-instant on mode of operation;
  - a plurality of input/output devices capable of being used in conjunction with said first plurality of electronic components during said instant on mode of operation and with said second plurality of electronic components during said non-instant on mode operation; and
  - one or more switching mechanisms to selectively couple one or more of said plurality of input/output devices to one or more of said first plurality of electronic components and enable said apparatus to start up in said instant on mode of operation to the exclusion of said second plurality of electronic components, or to selectively couple said one or more input/output devices to one or more of said second plurality of electronic components and enable said apparatus to start up in said non-instant on mode of operation to the exclusion of said first plurality of electronic components.
  
2. (Amended) [The apparatus of Claim 1,] An apparatus comprising:
  - a first plurality of electronic components defining an instant on mode of operation;
  - a second plurality of electronic components defining a non-instant on mode of operation;
  - a plurality of input/output devices wherein said first plurality of electronic components includes a first processor to execute instructions representing a first operating system and said second plurality of electronic components includes a second processor to execute instructions representing a second operating system; and
  - one or more switching mechanisms to selectively couple one or more of said plurality of input/output devices to one or more of said first plurality of electronic components and enable said apparatus to start up in said instant on mode of operation to the exclusion of said second plurality of electronic components, or to selectively couple said one or more input/output devices to one or more of said second plurality of electronic components and enable said apparatus to start up in said non-instant on mode of operation to the exclusion of said first plurality of electronic components.

3. (Twice Amended) [The apparatus of claim 2,] An apparatus comprising:

a first plurality of electronic components defining an instant on mode of operation wherein said first plurality of electronic components includes a first processor to execute instructions representing a first operating system;

a second plurality of electronic components defining a non-instant on mode of operation and including a second processor to execute instructions representing a second operating system, wherein said first plurality of electronic components includes a first memory device and said second plurality of electronic components includes a second memory device, and wherein after start up said first and second processors operate simultaneously to synchronize data between said first and second memory devices;

a plurality of input/output devices; and  
one or more switching mechanisms to selectively couple one or more of said plurality of input/output devices to one or more of said first plurality of electronic components and enable said apparatus to start up in said instant on mode of operation to the exclusion of said second plurality of electronic components, or to selectively couple said one or more input/output devices to one or more of said second plurality of electronic components and enable said apparatus to start up in said non-instant on mode of operation to the exclusion of said first plurality of electronic components.

4. (Twice Amended) [The apparatus of claim 1,] An apparatus comprising:

a first plurality of electronic components defining an instant on mode of operation;

a second plurality of electronic components defining a non-instant on mode of operation, wherein at least one of said first and second plurality of electronic components includes a processor having at least two operating modes, wherein when in a first operating mode said processor executes instructions representing a first operating system, and when in a second operating mode said processor executes instructions representing a second operating system;

a plurality of input/output devices; and  
one or more switching mechanisms to selectively couple one or more of said plurality of input/output devices to one or more of said first plurality of electronic components and enable said apparatus to start up in said instant on mode of operation to the exclusion of said second plurality of electronic components, or to selectively couple said one or more input/output devices to one or more of said second plurality of electronic components and enable said apparatus to

start up in said non-instant on mode of operation to the exclusion of said first plurality of electronic components.

6. (Amended) [The apparatus of claim 1,] An apparatus comprising:  
a first plurality of electronic components defining an instant on mode of operation;  
a second plurality of electronic components defining a non-instant on mode of operation;  
a plurality of input/output devices; and  
one or more switching mechanisms to selectively couple one or more of said plurality of input/output devices to one or more of said first plurality of electronic components and enable said apparatus to start up in said instant on mode of operation to the exclusion of said second plurality of electronic components, or to selectively couple said one or more input/output devices to one or more of said second plurality of electronic components and enable said apparatus to start up in said non-instant on mode of operation to the exclusion of said first plurality of electronic components wherein said one or more switching mechanisms includes a digital multiplexer.

8. (Twice Amended) An apparatus comprising:  
 an integrated circuit having a plurality of function blocks for use in a first instant on mode of operation;  
 a plurality of electronic components for use in a second non-instant on mode of operation;  
 a plurality of input and output devices capable of being used in conjunction with said plurality of function blocks during said first instant on mode of operation and with said plurality of electronic components during said second non-instant on mode of operation; and  
 one or more switching mechanisms to selectively couple one or more of said plurality of input and output devices to one or more of said function blocks to enable said one or more input and output devices to be available for use in said first instant on mode of operation to the exclusion of said plurality of electronic components, or to selectively couple said one or more of said plurality of input and output devices to one or more of said second plurality of electronic components to enable said one or more input and output devices to be available for use in said second non-instant on mode of operation to the exclusion of said plurality of function blocks.

9. (Amended) [The apparatus of claim 8,] An apparatus comprising:

an integrated circuit having a first plurality of function blocks for use in a first instant on mode of operation wherein said first plurality of function blocks includes a first processor to execute instructions representing a first operating system;

a plurality of electronic components for use in a second non-instant on mode of operation, and wherein said second plurality of electronic components includes a second processor to execute instructions representing a second operating system;

a plurality of input and output devices; and

one or more switching mechanisms to selectively couple one or more of said plurality of input and output devices to one or more of said function blocks to enable said one or more input and output devices to be available for use in said first instant on mode of operation to the exclusion of said plurality of electronic components, or to selectively couple said one or more of said plurality of input and output devices to one or more of said second plurality of electronic components to enable said one or more input and output devices to be available for use in said second non-instant on mode of operation to the exclusion of said first plurality of function blocks.

10. (Amended) [The apparatus of claim 9, further comprising] An apparatus comprising:

an integrated circuit having a plurality of function blocks for use in a first instant on mode of operation wherein said plurality of function blocks includes a first processor to execute instructions representing a first operating system;

a plurality of electronic components for use in a second non-instant on mode of operation wherein said second plurality of electronic components includes a second processor to execute instructions representing a second operating system;

a plurality of input and output devices;

one or more switching mechanisms to selectively couple one or more of said plurality of input and output devices to one or more of said function blocks to enable said one or more input and output devices to be available for use in said first instant on mode of operation to the exclusion of said plurality of electronic components, or to selectively couple said one or more of said plurality of input and output devices to one or more of said second plurality of electronic components to enable said one or more input and output devices to be available for use in said second non-instant on mode of operation to the exclusion of said plurality of function blocks; and

a connector interface to couple said one or more switching mechanisms to said integrated circuit.

11. (Twice Amended) [The apparatus of claim 9, wherein said plurality of function block includes] An apparatus comprising:

an integrated circuit having a plurality of function blocks for use in a first instant on mode of operation wherein said plurality of function blocks includes a first memory device and a first processor to execute instructions representing a first operating system [, wherein said second plurality of electronic components includes];

a plurality of electronic components for use in a second non-instant on mode of operation wherein said second plurality of electronic components includes a second memory device and a second processor to execute instructions representing a second operating system and wherein said first and second processors operate simultaneously to synchronize data stored within said first and second memory devices;

a plurality of input and output devices; and

one or more switching mechanisms to selectively couple one or more of said plurality of input and output devices to one or more of said function blocks to enable said one or more input and output devices to be available for use in said first instant on mode of operation to the exclusion of said plurality of electronic components, or to selectively couple said one or more of said plurality of input and output devices to one or more of said second plurality of electronic components to enable said one or more input and output devices to be available for use in said second non-instant on mode of operation to the exclusion of said plurality of function blocks.

12 (Twice Amended). [The apparatus of claim 8,] An apparatus comprising:

an integrated circuit having a plurality of function blocks for use in first instant on mode of operation;

a plurality of electronic components for use in a second non-instant on mode of operation wherein at least one of said plurality of function blocks and said plurality of electronic components includes a processor having at least two operating modes, wherein when in a first operating mode, said processor executes instructions representing a first operating system, and when in a second operating mode, said processor executes instructions representing a second operating system;

a plurality of input and output devices; and

one or more switching mechanisms to selectively couple one or more of said plurality of input and output devices to one or more of said function blocks to enable said one or more input and output devices to be available for use in said first instant on mode of operation to the exclusion of said plurality of electronic components, or to selectively couple said one or more of said plurality of input and output devices to one or more of said second plurality of electronic components to enable said one or more input and output devices to be available for use in said second non-instant on mode of operation to the exclusion of said plurality of function blocks.

14. (Amended) [The apparatus of claim 8,] An apparatus comprising:

an integrated circuit having a plurality of function blocks for use in a first instant on mode of operation;

a plurality of electronic components for use in a second non-instant on mode of operation;

a plurality of input and output devices wherein said plurality of input and output devices includes a user input device and a display device; and

one or more switching mechanisms to selectively couple one or more of said plurality of input and output devices to one or more of said function blocks to enable said one or more input and output devices to be available for use in said first instant on mode of operation to the exclusion of said plurality of electronic components, or to selectively couple said one or more of said plurality of input and output devices to one or more of said second plurality of electronic components to enable said one or more input and output devices to be available for use in said second non-instant on mode of operation to the exclusion of said plurality of function blocks.

17. (Amended) [The integrated circuit of claim 16,] An integrated circuit comprising:

a first processor block to operate in a first instant on mode of operation;

a second processor block to operate in a second non-instant on mode of operation;

a plurality of input/output ports; and

one or more switching mechanisms to selectively couple one or more external devices to said first processor block through at least one of said plurality of input/output ports to facilitate use of said one or more external devices in said instant on mode of operation to the exclusion of said second processor block, or to selectively couple said one or more external devices to said second processor block through at least one of said plurality of input/output ports to facilitate use



of said one or more external devices in said non instant on mode of operation to the exclusion of said first processor block wherein said plurality of external devices includes a user input device and a display device.

18. (Twice Amended) [The integrated circuit of claim 16,] An integrated circuit comprising:  
a first processor block to operate in a first instant on mode of operation;  
a second processor block to operate in a second non-instant on mode of operation;  
a plurality of input/output ports; and  
one or more switching mechanisms to selectively couple one or more external devices to said first processor block through at least one of said plurality of input/output ports to facilitate use of said one or more external devices in said instant on mode of operation to the exclusion of said second processor block, or to selectively couple said one or more external devices to said second processor block through at least one of said plurality of input/output ports to facilitate use of said one or more external devices in said non instant on mode of operation to the exclusion of said first processor block wherein said [plurality of] one or more external devices includes a first memory device and a second memory device, and wherein after start up said first and second processor blocks operate simultaneously to synchronize data between said first and second memory devices.

20. [The integrated circuit of claim 16,] An integrated circuit comprising:  
a first processor block to operate in a first instant on mode of operation;  
a second processor block to operate in a second non-instant on mode of operation;  
a plurality of input/output ports; and  
one or more switching mechanisms to selectively couple one or more external devices to said first processor block through at least one of said plurality of input/output ports to facilitate use of said one or more external devices in said instant on mode of operation to the exclusion of said second processor block, or to selectively couple said one or more external devices to said second processor block through at least one of said plurality of input/output ports to facilitate use of said one or more external devices in said non instant on mode of operation to the exclusion of said first processor block wherein said one or more switching mechanisms includes a digital multiplexor.